

preliminary

CMA033N12NH-TL

## 120V N-Channel Power MOSFET

### Features

- High Speed Power Switching
- Enhanced Body diode dv/dt capability
- Enhanced Avalanche Ruggedness
- 100% UIS Tested, 100% Rg Tested
- Lead Free, Halogen Free

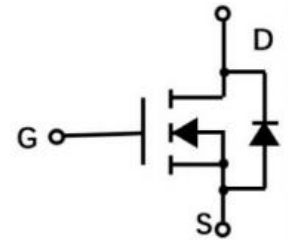
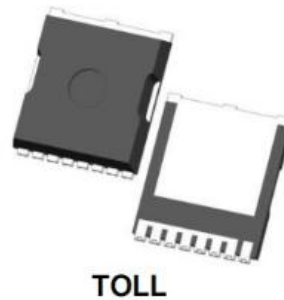
### Applications

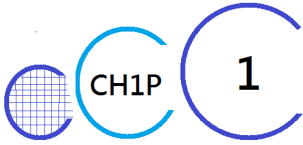
- DC/DC Converter
- Motor Control
- Power management
- Synchronous Rectification

### Product Summary

Item	Typical Value	Unit
$V_{DS}$	120	V
$R_{DS(on)} @ V_{GS} = 10V (Max)$	3.3	m $\Omega$
$I_D$	225	A

### Pin Description





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**Absolute Ratings** ( $T_A = 25^\circ\text{C}$  unless otherwise noted)

Parameter	Symbol	Value	Units
Drain-Source Voltage	$V_{DS}$	120	V
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Single Pulse UIS Capability, 0.5mH	$E_{AS}$	625	mJ
Continuous Drain Current, $T_C = 25^\circ\text{C}/100^\circ\text{C}$	$I_D$	225/159	A
Maximum Power Dissipation, $T_C = 25^\circ\text{C}$	$P_D$	365.8	W
Junction Temperature Maximum	$T_{JMAX}$	175	$^\circ\text{C}$
Storage Temperature	$T_{Storage}$	-55 to 175	$^\circ\text{C}$

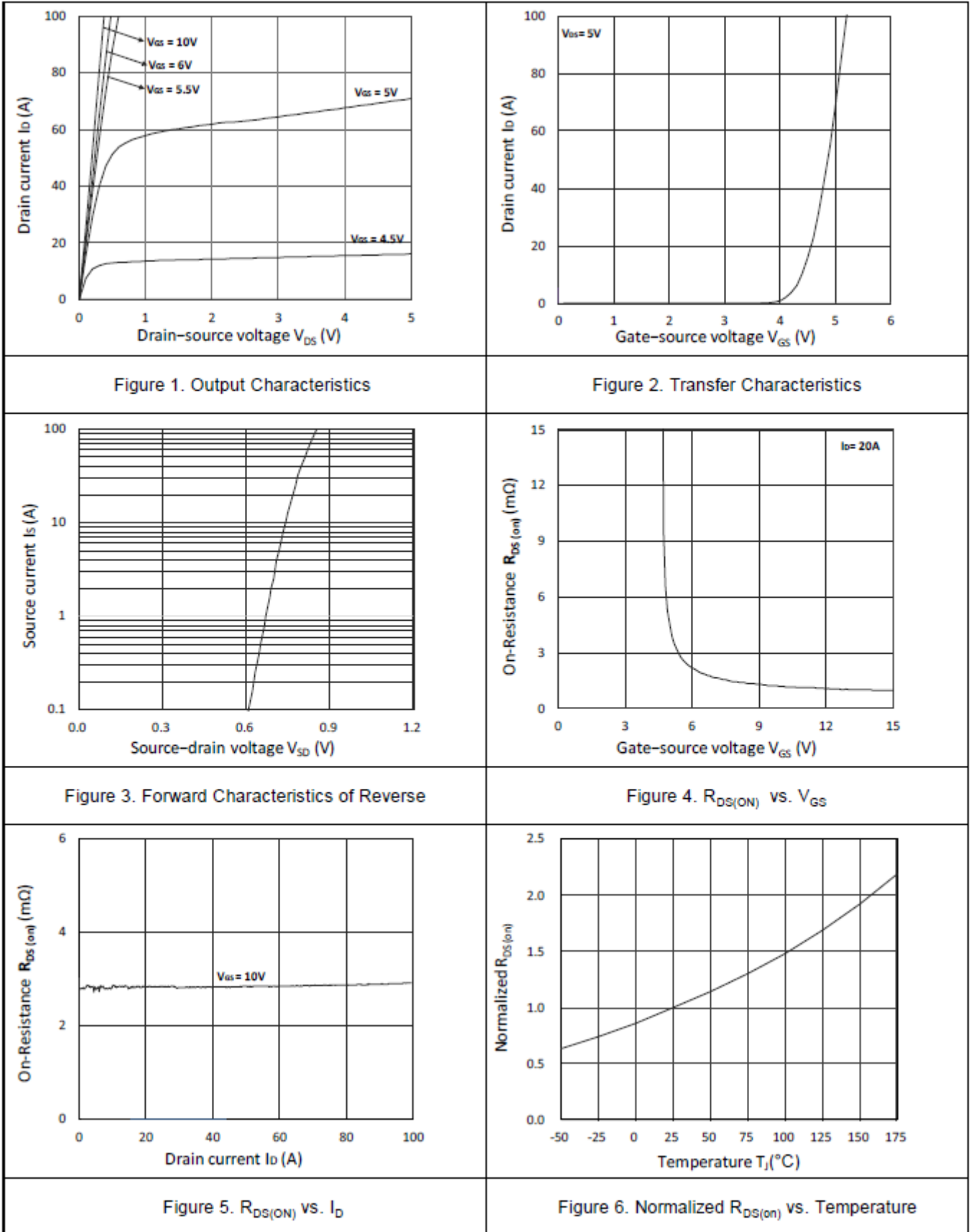
**Absolute Ratings**

Parameter	Symbol	Value	Units
Thermal Resistance Junction-Ambient	$R_{\theta JA}$	40	$^\circ\text{C}/\text{W}$

### Electrical Characteristics

Static ( $T_J=25^\circ\text{C}$ unless otherwise specified)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain-Source Breakdown Voltage	$BV_{DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	120	---	---	V
Gate-Source Leakage	$I_{GSS}$	$V_{GS} = \pm 20V, V_{DS} = 0V$	---	---	$\pm 100$	nA
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 120V, V_{GS} = 0V, T_J=25^\circ\text{C}$	---	---	1	uA
		$V_{DS} = 120V, V_{GS} = 0V, T_J=100^\circ\text{C}$	---	---	100	
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 20A$	---	---	3.3	m $\Omega$
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.5	---	3.5	V
Dynamic ( $T_J=25^\circ\text{C}$ unless otherwise specified)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Input Capacitance	$C_{iss}$	$V_{GS} = 0V, V_{DS} = 60V, f = 1\text{MHz}$	---	8690	---	pF
Output Capacitance	$C_{oss}$		---	733	---	
Reverse Transfer Capacitance	$C_{rss}$		---	16	---	
Total Gate Charge	$Q_g$	$V_{DS} = 60V, I_D = 20A, V_{GS} = 10V$	---	144	---	nC
Gate-Source Charge	$Q_{gs}$		---	38	---	
Gate-Drain Charge	$Q_{gd}$		---	37	---	
Turn-on delay time	$T_{d(on)}$	$V_{DS} = 60V, I_D = 20A, V_{GS} = 10V, R_G = 3\Omega,$	---	29.6	---	ns
Rise time	$T_r$		---	50.6	---	
Turn-off delay time	$T_{d(off)}$		---	89	---	
Fall time	$T_f$		---	54	---	
Reverse Diode Characteristics						
Diode Forward Voltage	$V_{SD}$	$V_{GS} = 0V, I_F = 20A$	---	---	1.2	V
Reverse Recovery Time	$t_{rr}$	$V_{GS} = 0V, I_F = 20A, dI_F/dt=100A/\mu s$	---	90	---	ns
Reverse Recovery Charge	$Q_{rr}$		---	217	---	nC

Typical Characteristics



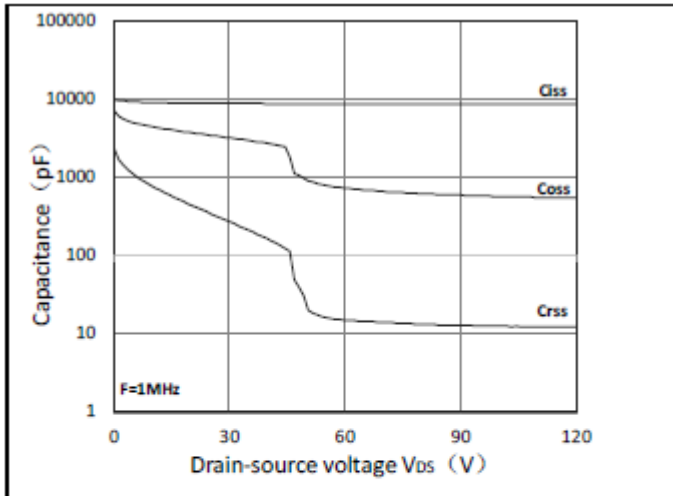


Figure 7. Capacitance Characteristics

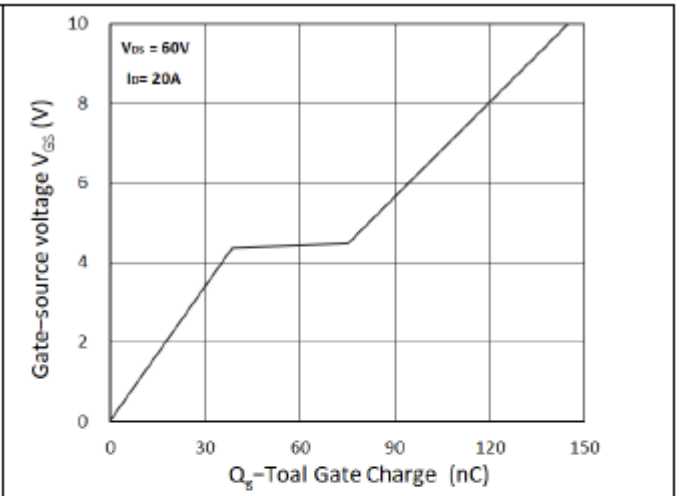


Figure 8. Gate Charge Characteristics

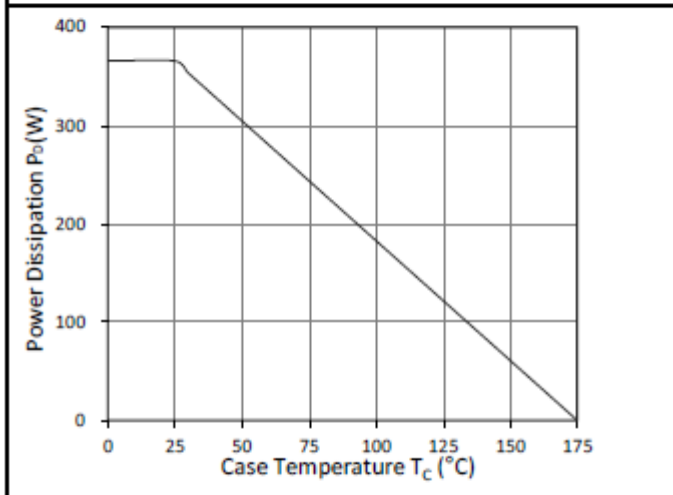


Figure 9. Power Dissipation

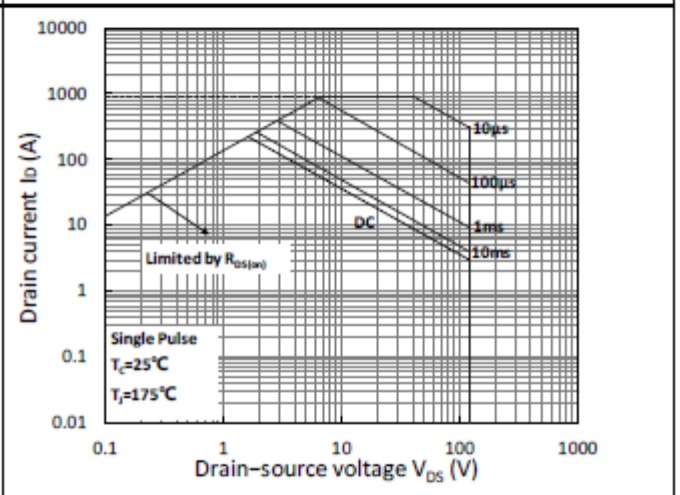


Figure 10. Safe Operating Area

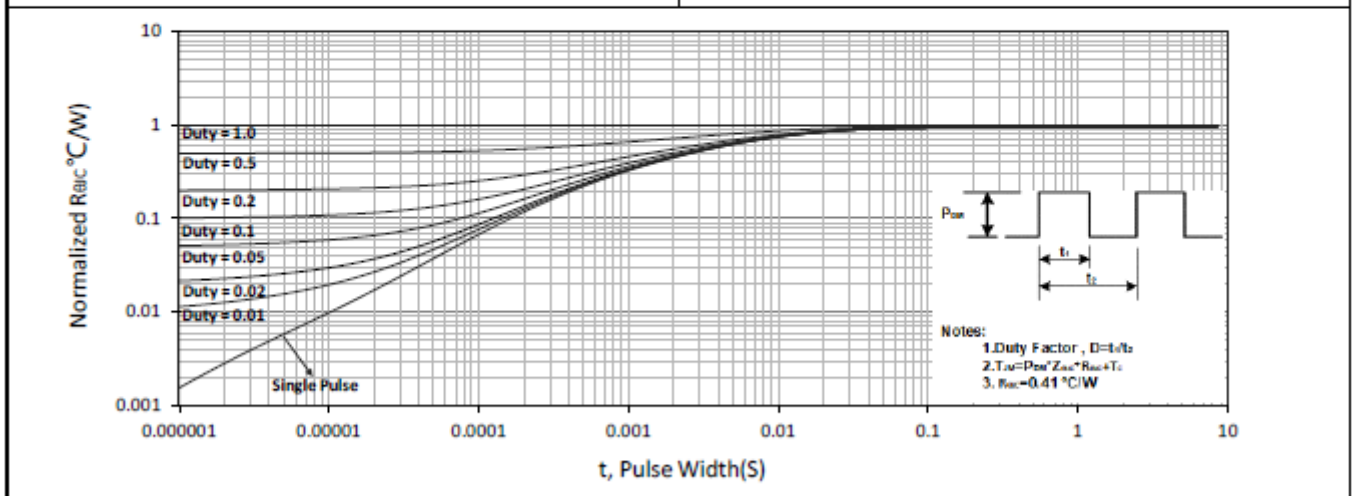


Figure 11. Normalized Maximum Transient Thermal Impedance

