

### CMA010N04NH-TL

### **40V N-Channel Power MOSFET**

#### **Features**

- High Speed Power Switching
- Enhanced Body diode dv/dt capability
- Enhanced Avalanche Ruggedness
- 100% UIS Tested, 100% Rg Tested
- Lead Free, Halogen Free

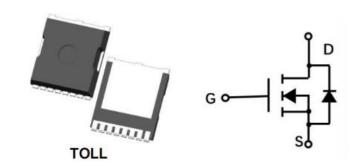
## **Applications**

- Synchronous Rectification in SMPS
- High Frequency Switching

### **Product Summary**

Item	Typical Value	Unit	
V <sub>DS</sub>	40	٧	
R <sub>DS(on)</sub> @ V <sub>GS</sub> =10V (Max)	1.0	mΩ	
I <sub>D</sub>	300	А	

### **Pin Description**





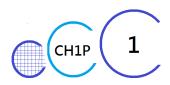
# CMA010N04NH-TL

# **Absolute Ratings** ( $T_A = 25^{\circ}C$ unless otherwise noted)

Parameter	Symbol	Value	Units	
Drain-Source Voltage	V <sub>DS</sub>	40	V	
Gate-Source Voltage	V <sub>GS</sub>	±20	V	
Single Pulse UIS Capability, 1.0mH	E <sub>AS</sub>	287	mJ	
Continuous Drain Current, T <sub>C</sub> = 25°C/100°C	ΙD	300/130	Α	
Maximum Power Dissipation, T <sub>C</sub> = 25°C	P <sub>D</sub>	192	W	
Junction Temperature Maximum	Т <sub>ЈМАХ</sub>	150	°C	
Storage Temperature	T <sub>Storage</sub>	-55 to 150	°C	

# **Absolute Ratings**

Parameter	Symbol	Value	Units
Thermal Resistance Junction-Ambient	$R_{ heta}$ JA	18	°C/W



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### **Flectrical Characteristics**

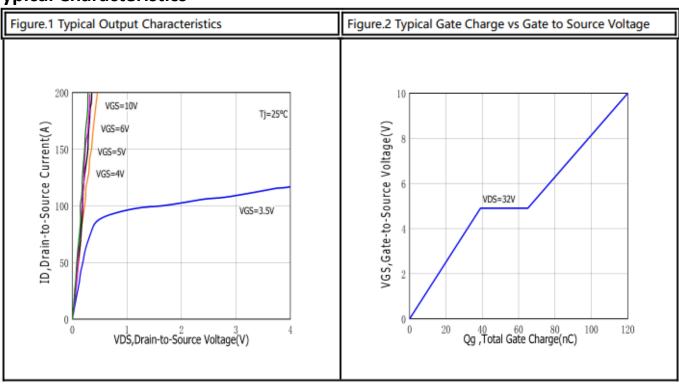
Electrical Characteristics							
Static (T <sub>J</sub> =25°C unless otherwise specified)							
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units	
Drain-Source Breakdown Voltage	BV <sub>DSS</sub>	V <sub>GS</sub> = 0V, I <sub>D</sub> = 250uA	40			V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{GS} = \pm 20V, V_{DS} = 0V$			±100	nA	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 40V, V <sub>GS</sub> = 0V,T <sub>J</sub> =25°C			1	uA	
		$V_{DS} = 40V, V_{GS} = 0V, T_{J} = 100^{\circ}C$			100		
Drain-Source On-State Resistance	R <sub>DS(on)</sub>	$V_{GS} = 10V, I_D = 75A$			1.0	mΩ	
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250uA	2.0		4.0	V	
	• /=	0500	n\				
Dynamic (T」=25°C unless otherwise specified)         Parameter       Symbol       Test Conditions       Min.       Typ.       Max.       Units							
Parameter	Symbol C <sub>iss</sub>	Test Conditions	Min.	<b>Typ.</b> 6450	Max.	Units	
Input Capacitance	Coss	$V_{GS} = 0V, V_{DS} = 10 V,$		3610		pF	
Output Capacitance	+	f = 300kHz					
Reverse Transfer Capacitance	Crss			330			
Total Gate Charge	Qg	V <sub>DS</sub> = 32V, I <sub>D</sub> = 150A, V <sub>GS</sub> = 10V		120			
Gate-Source Charge	$Q_{gs}$			39		nC	
Gate-Drain Charge	$Q_gd$			26			
Turn-on delay time	$T_{d(on)}$			19			
Rise time	Tr	$V_{DS} = 17.5V, R_L = 0.235\Omega, \ V_{GS} = 10V, R_G = 5\Omega,$		26			
Turn-off delay time	T <sub>d(off)</sub>			85		ns	
Fall time	Tf			45			
	Reve	rse Diode Characteristics					
Diode Forward Voltage	V <sub>SD</sub>	V <sub>GS</sub> = 0V, I <sub>S</sub> = 20A			1.2	V	
Reverse Recovery Time	t <sub>rr</sub>	$V_D = 35V, I_F = 10A,$		141		ns	
Reverse Recovery Charge	Qrr	dl <sub>F</sub> /dt=100A/us		333		nC	

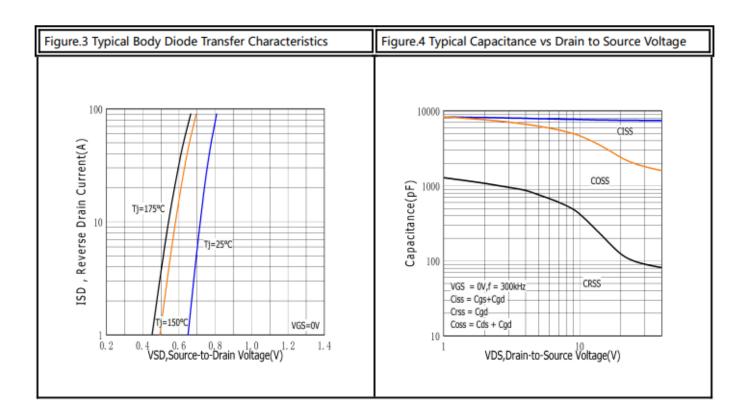


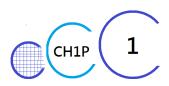




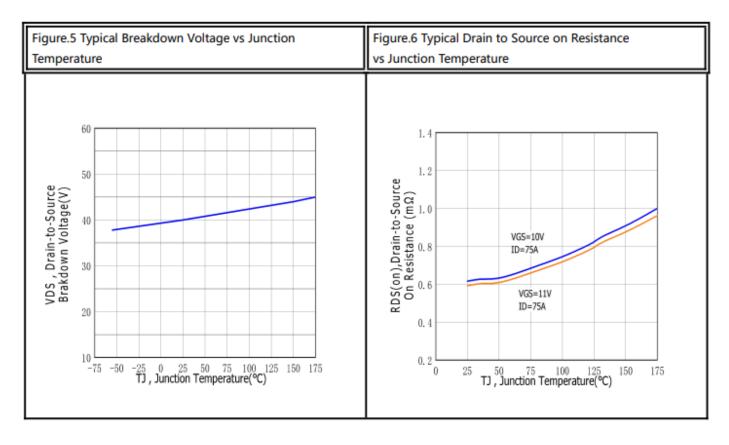
### **Typical Characteristics**

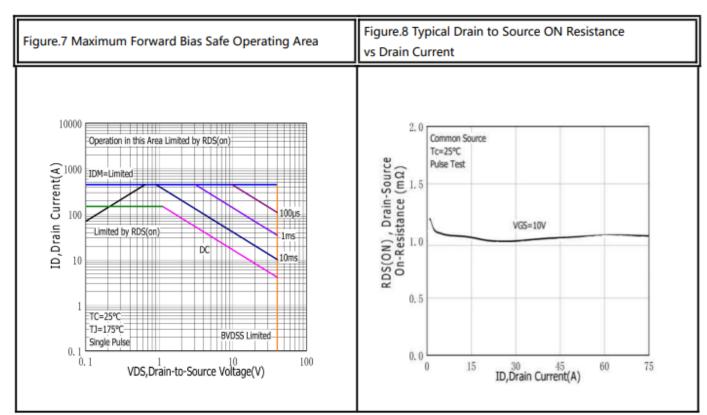






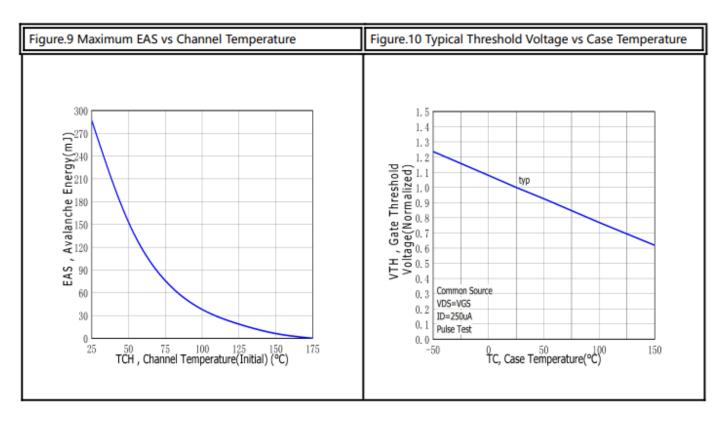
### CMA010N04NH-TL

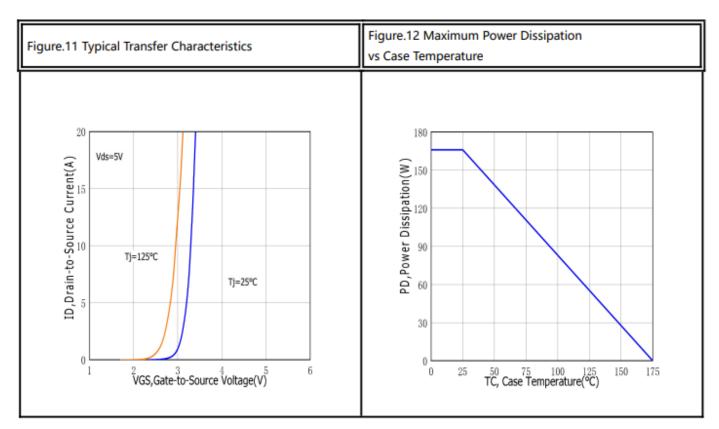






### CMA010N04NH-TL







30

0

50 75 100 125 TC, Case Temperature(°C)

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### CMA010N04NH-TL

1E-4 0. 001 0. 01 Pulse Time(s)

1E-6

1E-5

Figure.13 Maximum Drain Current vs. Case Temperature

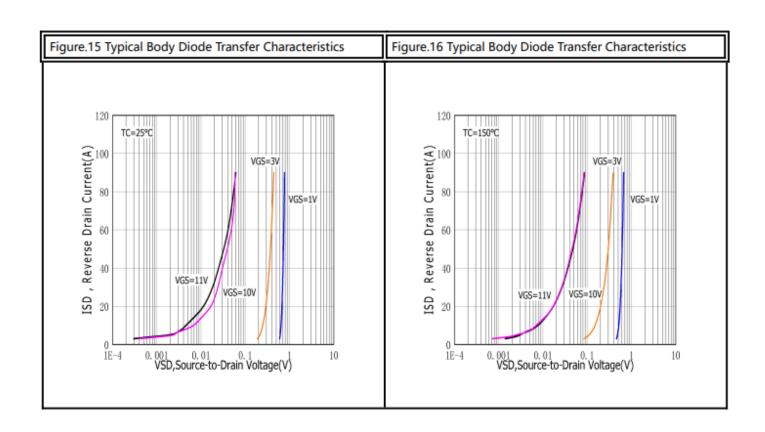
Figure.13 Maximum Effective Thermal Impedance , Junction to Case

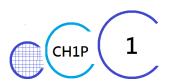
Figure.14 Maximum Effective Thermal Impedance , Junction to Case

Figure.15 Maximum Effective Thermal Impedance , Junction to Case

Figure.16 Maximum Effective Thermal Impedance , Junction to Case

Figure.17 Maximum Effective Thermal Impedance , Junction to Case





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# **TOLL**

