

## preliminary

#### CMY12603PL-S8

## **30V P-Channel Fast Switching MOSFET**

#### **Features**

- Advanced Trench MOS Technology
- 100% EAS Guaranteed
- Green Device Available
- Low R<sub>DS(ON)</sub>

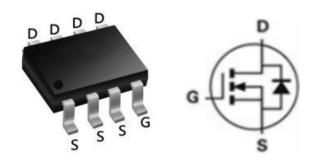
#### **Product Summary**

Item	Typical Value	Unit
V <sub>DS</sub> @ T <sub>JMAX</sub>	-30	٧
R <sub>DS(on)</sub> @ V <sub>GS</sub> =-10V (Max)	20	mΩ
I <sub>D</sub>	-9.5	А

### **Applications**

- Load Switches
- Hard Swithing and High Speed Circuit
- BLDC Motor

### **SOP8 Pin Description**



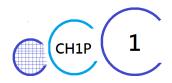


### **Absolute Ratings** ( $T_A = 25^{\circ}C$ unless otherwise noted)

	<u> </u>		
Parameter	Symbol	Value	Units
Drain-Source Voltage	V <sub>DS</sub>	-30	V
Gate-Source Voltage	V <sub>G</sub> S	±20	V
Pulse Drain Current Tested	I <sub>DM</sub>	-50	Α
Continuous Drain Current, T <sub>C</sub> = 25°C/70°C	I <sub>D</sub>	-9.5/-7.6	Α
Single Pulse UIS Capability, 0.1mH	Eas	72	mJ
Maximum Power Dissipation	PD	3.1	W
Junction Temperature Maximum	Тлмах	150	°C
Storage Temperature	T <sub>Storage</sub>	-55 to 150	°C

#### **Thermal Characteristics**

Parameter	Symbol	Value(Max)	Units
Thermal Resistance Junction to ambient	$R_{ heta}JA$	75	°C/W



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### **Electrical Characteristics**

	Static (T.	=25°C unless otherwise specified)				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Drain-Source Breakdown Voltage	BVDSS	V <sub>GS</sub> = 0V, I <sub>D</sub> = -250uA	-30			V
Gate-Source Leakage	Igss	V <sub>GS</sub> = ±20V, V <sub>DS</sub> = 0V			±100	nA
Zero Gate Voltage Drain Current	IDSS	V <sub>DS</sub> = -24V, V <sub>GS</sub> = 0V			-1	uA
	_	V <sub>GS</sub> = -10V, I <sub>D</sub> = -6A			20	mΩ
Drain-Source On-State Resistance	RDS(on)	V <sub>GS</sub> = -4.5V, I <sub>D</sub> = -4A			32	
Gate-Source Threshold Voltage	VGS(th)	$V_{DS} = V_{GS}$ , $I_D = -250uA$	-1.0		-2.5	V
	Dynamic (	T <sub>J</sub> =25°C unless otherwise specified				
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Units
Input Capacitance	Ciss			1345		pF
Output Capacitance	Coss	$V_{GS} = 0V, V_{DS} = -15 V,$ f = 1MHz		194		
Reverse Transfer Capacitance	Crss			158		
Total Gate Charge	Qg			12.6		
Gate-Source Charge	Qgs	$V_{DS} = -15V, I_{D} = -6A,$ $V_{GS} = -4.5 V$		4.8		nC
Gate-Drain Charge	Qgd			4.8		
Turn-on delay time	T <sub>d(on)</sub>			4.6		
Rise time	Tr	$V_{DD} = -15V, I_{D} = -6A,$		14.8		
Turn-off delay time	T <sub>d(off)</sub>	$V_{GS}$ = -10V, $R_{G}$ =3.3 $\Omega$		41		nS
Fall time	Tf			19.6		
	1					
	Reve	rse Diode Characteristics				
Continuous Source Current	Is	V <sub>G</sub> = V <sub>D</sub> = 0V, Force Current			-9.5	Α
Diode Forward Voltage	V <sub>SD</sub>	IS = -1A, VGS = 0V			-1.2	V
Reverse Recovery Time	t <sub>rr</sub>	$V_{GS} = 0V, I_{F} = -6A,$		16.3		ns
Reverse Recovery Charge	Qrr	dl <sub>F</sub> /dt=100A/us		5.9		nC



#### **Typical Characteristics**

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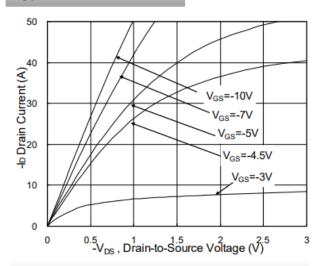


Fig.1 Typical Output Characteristics

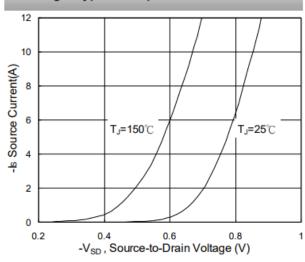


Fig.3 Forward Characteristics of Reverse

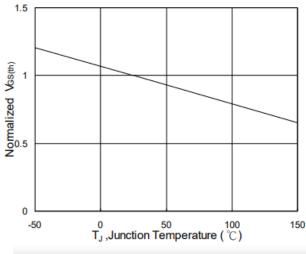


Fig.5 Normalized V<sub>GS(th)</sub> vs. T<sub>J</sub>

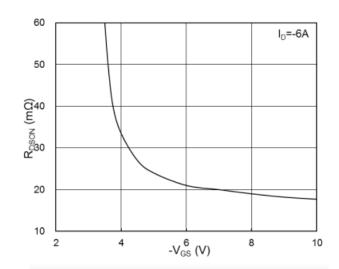


Fig.2 On-Resistance v.s Gate-Source

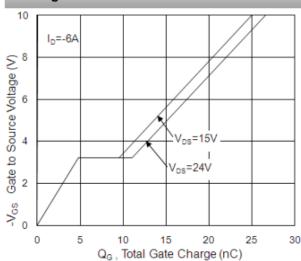


Fig.4 Gate-Charge Characteristics

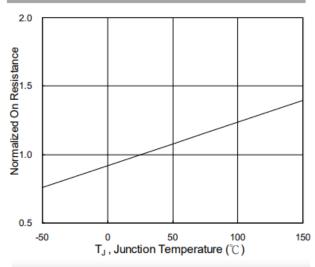
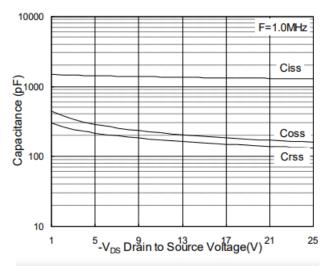


Fig.6 Normalized RDSON vs. TJ



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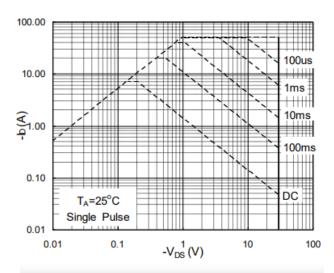


Fig.7 Capacitance

Fig.8 Safe Operating Area

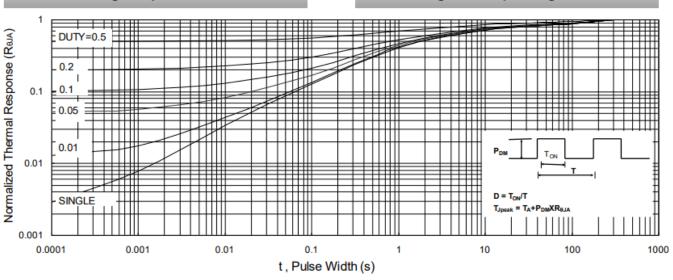
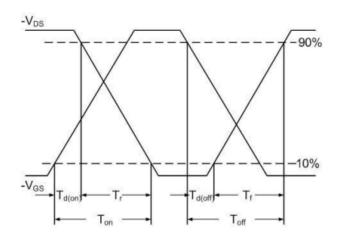


Fig.9 Normalized Maximum Transient Thermal Impedance



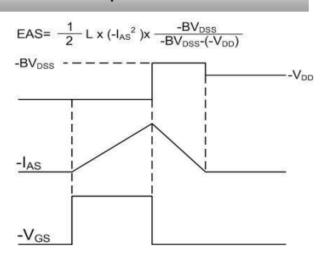
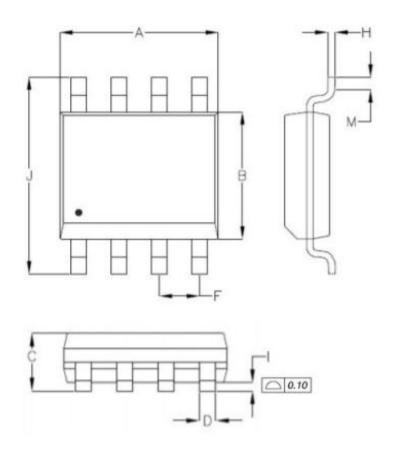


Fig.10 Switching Time Waveform

Fig.11 Unclamped Inductive Switching Waveform



# SOP-8L Package Outline



SYMBOLS	MILLIM	ETERS	INC	HES
	MIN	MAX	MIN	MAX
А	4.700	5.150	0.185	0.203
В	3.700	4.100	0.146	0.161
С	1.23	1.753	0.048	0.069
D	0.310	0.510	0.012	0.020
F	1.070	1.470	0.042	0.058
Н	0.160	0.254	0.006	0.010
1	0.050	0.254	0.002	0.010
J	5.750	6.250	0.226	0.246
M	0.400	1.270	0.016	0.050