

preliminary

CMY9803PL-S8

30V P-Channel Fast Switching MOSFET

Features

- Advanced Trench MOS Technology
- 100% EAS Guaranteed
- Green Device Available
- Low $R_{DS(on)}$

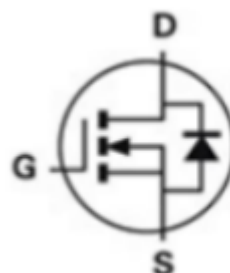
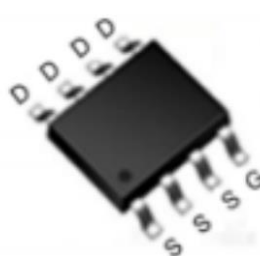
Product Summary

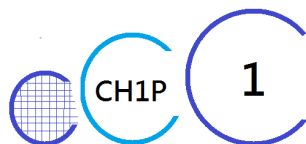
Item	Typical Value	Unit
$V_{DS} @ T_{JMAX}$	-30	V
$R_{DS(on)} @ V_{GS} = -10V (Max)$	30	m Ω
I_D	-6	A

Applications

- Load Switches
- Hard Switching and High Speed Circuit
- BLDC Motor

SOP8 Pin Description





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Absolute Ratings ($T_A = 25^{\circ}\text{C}$ unless otherwise noted)

Parameter	Symbol	Value	Units
Drain-Source Voltage	V_{DS}	-30	V
Gate-Source Voltage	V_{GS}	± 20	V
Pulse Drain Current Tested	I_{DM}	-24	A
Continuous Drain Current, $T_C = 25^{\circ}\text{C}/70^{\circ}\text{C}$	I_D	-6/-4.7	A
Maximum Power Dissipation	P_D	1.5	W
Junction Temperature Maximum	T_{JMAX}	150	$^{\circ}\text{C}$
Storage Temperature	$T_{Storage}$	-55 to 150	$^{\circ}\text{C}$

Thermal Characteristics

Parameter	Symbol	Value(Max)	Units
Thermal Resistance Junction to ambient	$R_{\theta JA}$	85	$^{\circ}\text{C}/\text{W}$

Electrical Characteristics

Static ($T_J=25^{\circ}\text{C}$ unless otherwise specified)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = -250\mu A$	-30	---	---	V
Gate-Source Leakage	I_{GSS}	$V_{GS} = \pm 20V, V_{DS} = 0V$	---	---	± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{DS} = -24V, V_{GS} = 0V$	---	---	-1	μA
Drain-Source On-State Resistance	$R_{DS(on)}$	$V_{GS} = -10V, I_D = -6A$	---	---	30	m Ω
		$V_{GS} = -4.5V, I_D = -3A$	---	---	55	
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = -250\mu A$	-1.0	---	-2.5	V
Continuous Source Current	I_S	$V_G = V_D = 0V$, Force Current	---	---	-6	A
Diode Forward Voltage	V_{SD}	$I_S = 1A, V_{GS} = 0V$	---	---	-1.2	V
Dynamic ($T_J=25^{\circ}\text{C}$ unless otherwise specified)						
Parameter	Symbol	Test Conditions	Min.	Typ.	Max.	Units
Input Capacitance	C_{iss}	$V_{GS} = 0V, V_{DS} = -15V$, $f = 1MHz$	---	930	---	pF
Output Capacitance	C_{oss}		---	148	---	
Reverse Transfer Capacitance	C_{rss}		---	115	---	
Total Gate Charge	Q_g	$V_{DS} = -20V, I_D = -6A$, $V_{GS} = -4.5V$	---	9.8	---	nC
Gate-Source Charge	Q_{gs}		---	2.2	---	
Gate-Drain Charge	Q_{gd}		---	3.4	---	
Turn-on delay time	$T_{d(on)}$	$V_{DD} = -24V, I_D = -1A$, $V_{GS} = -10V, R_G = 3.3\Omega$	---	16.4	---	nS
Rise time	T_r		---	20.2	---	
Turn-off delay time	$T_{d(off)}$		---	55	---	
Fall time	T_f		---	10	---	

Typical Characteristics

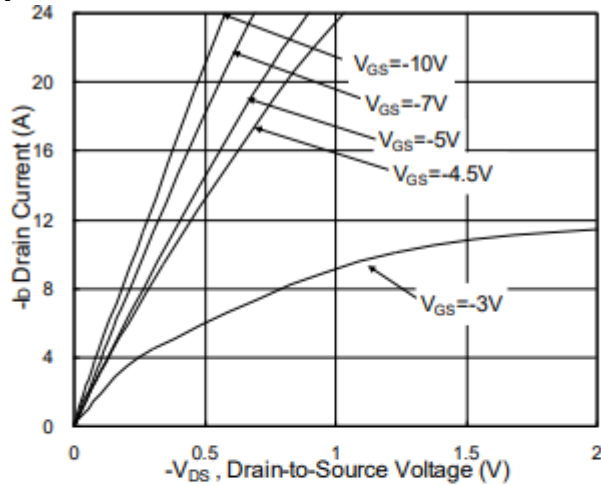


Fig.1 Typical Output Characteristics

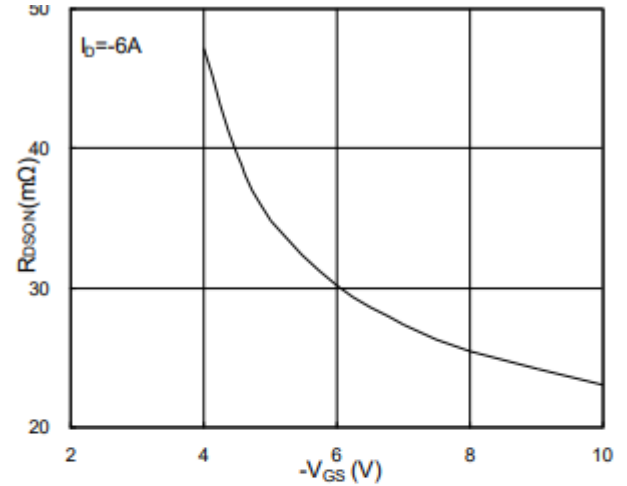


Fig.2 On-Resistance v.s Gate-Source

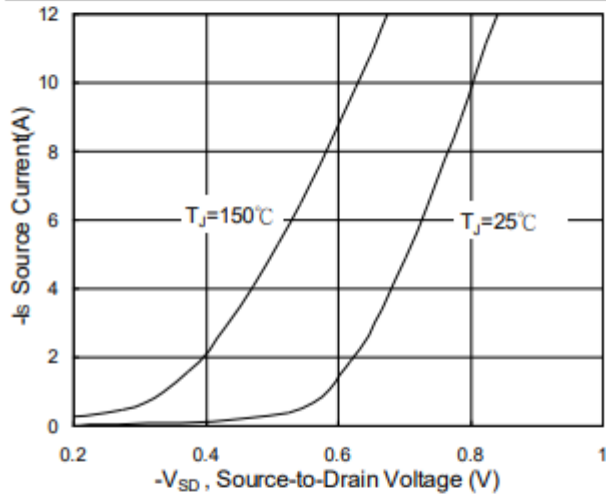


Fig.3 Forward Characteristics of Reverse

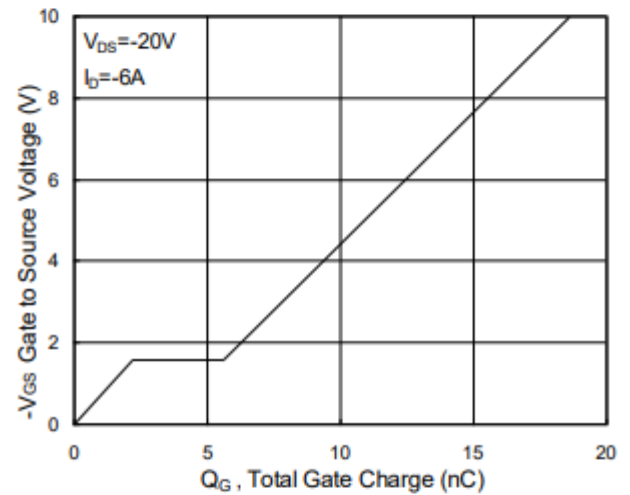


Fig.4 Gate-Charge Characteristics

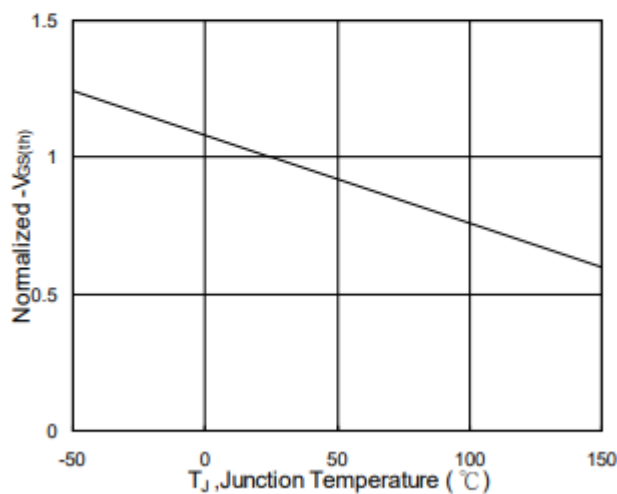


Fig.5 Normalized $V_{GS(th)}$ v.s T_J

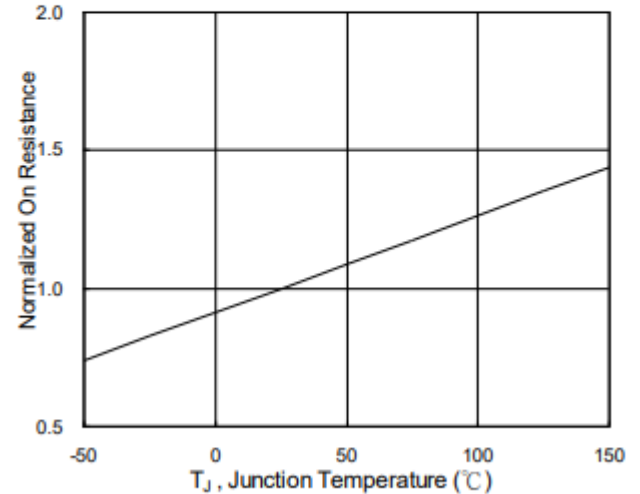


Fig.6 Normalized $R_{DS(on)}$ v.s T_J

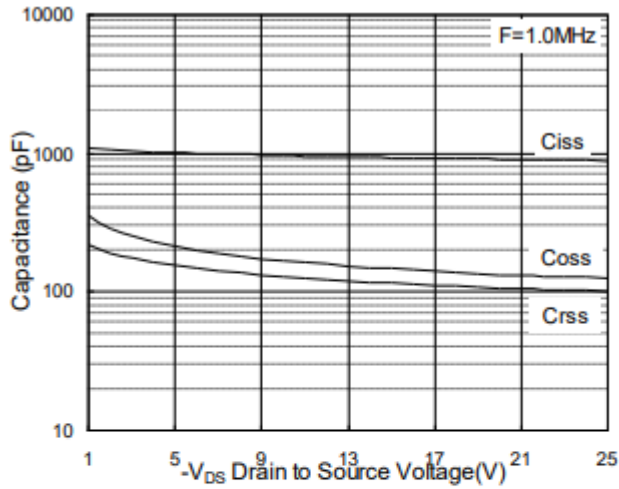


Fig.7 Capacitance

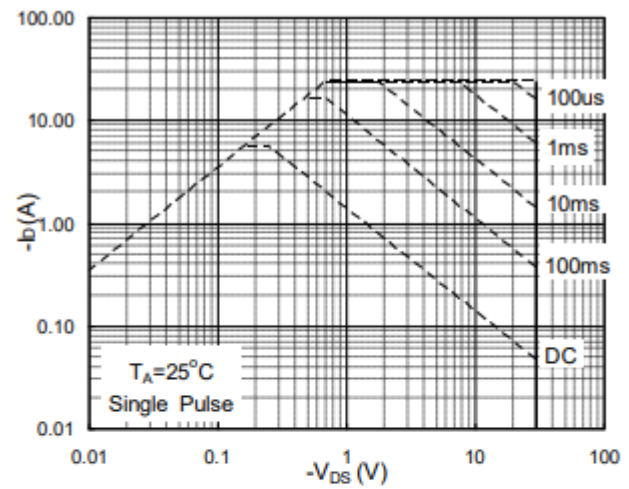


Fig.8 Safe Operating Area

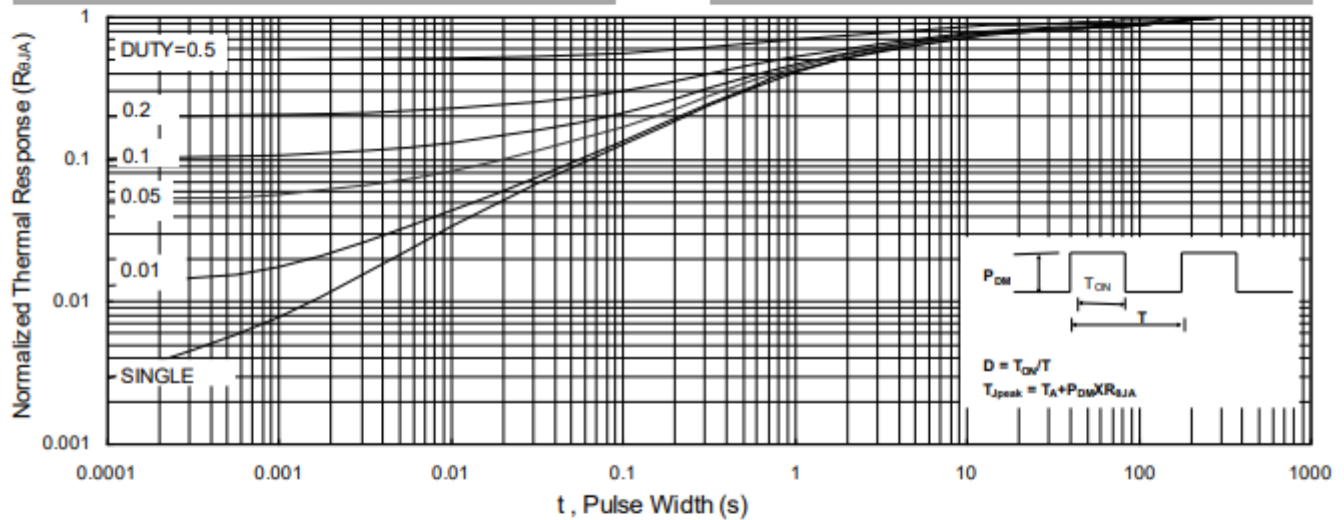


Fig.9 Normalized Maximum Transient Thermal Impedance

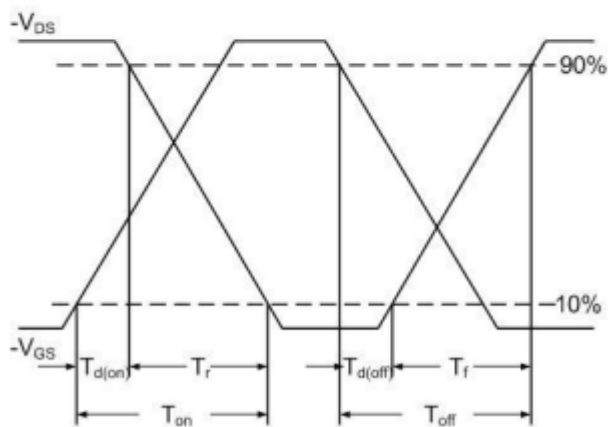


Fig.10 Switching Time Waveform

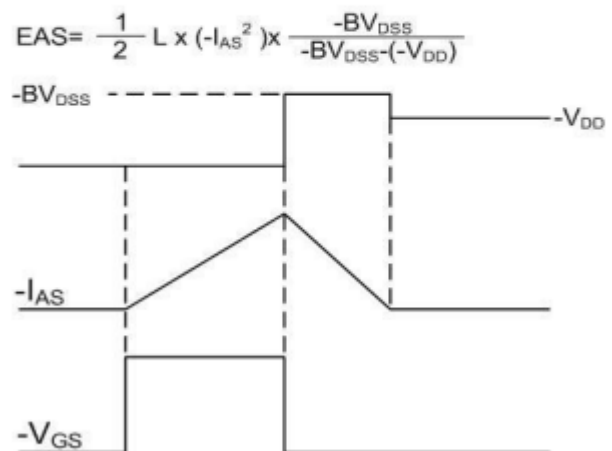
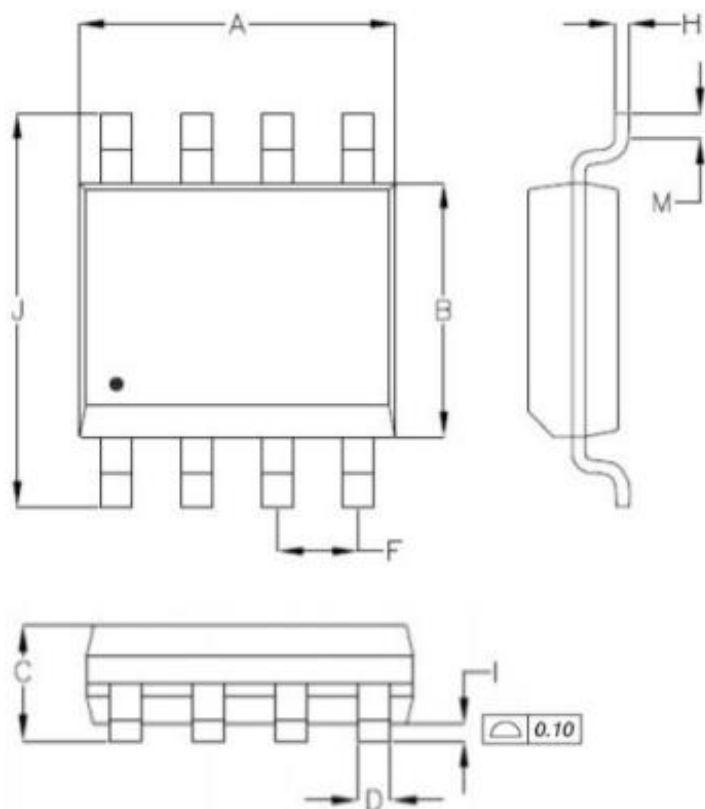


Fig.11 Unclamped Inductive Switching Waveform

SOP-8L Package Outline



SYMBOLS	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	4.700	5.150	0.185	0.203
B	3.700	4.100	0.146	0.161
C	1.23	1.753	0.048	0.069
D	0.310	0.510	0.012	0.020
F	1.070	1.470	0.042	0.058
H	0.160	0.254	0.006	0.010
I	0.050	0.254	0.002	0.010
J	5.750	6.250	0.226	0.246
M	0.400	1.270	0.016	0.050